

Logické obvody schémata

ELT pro E2

Stanovte stav na výstupu

▶ $A = 0$

▶ $B = 1$

▶ $C = 1$

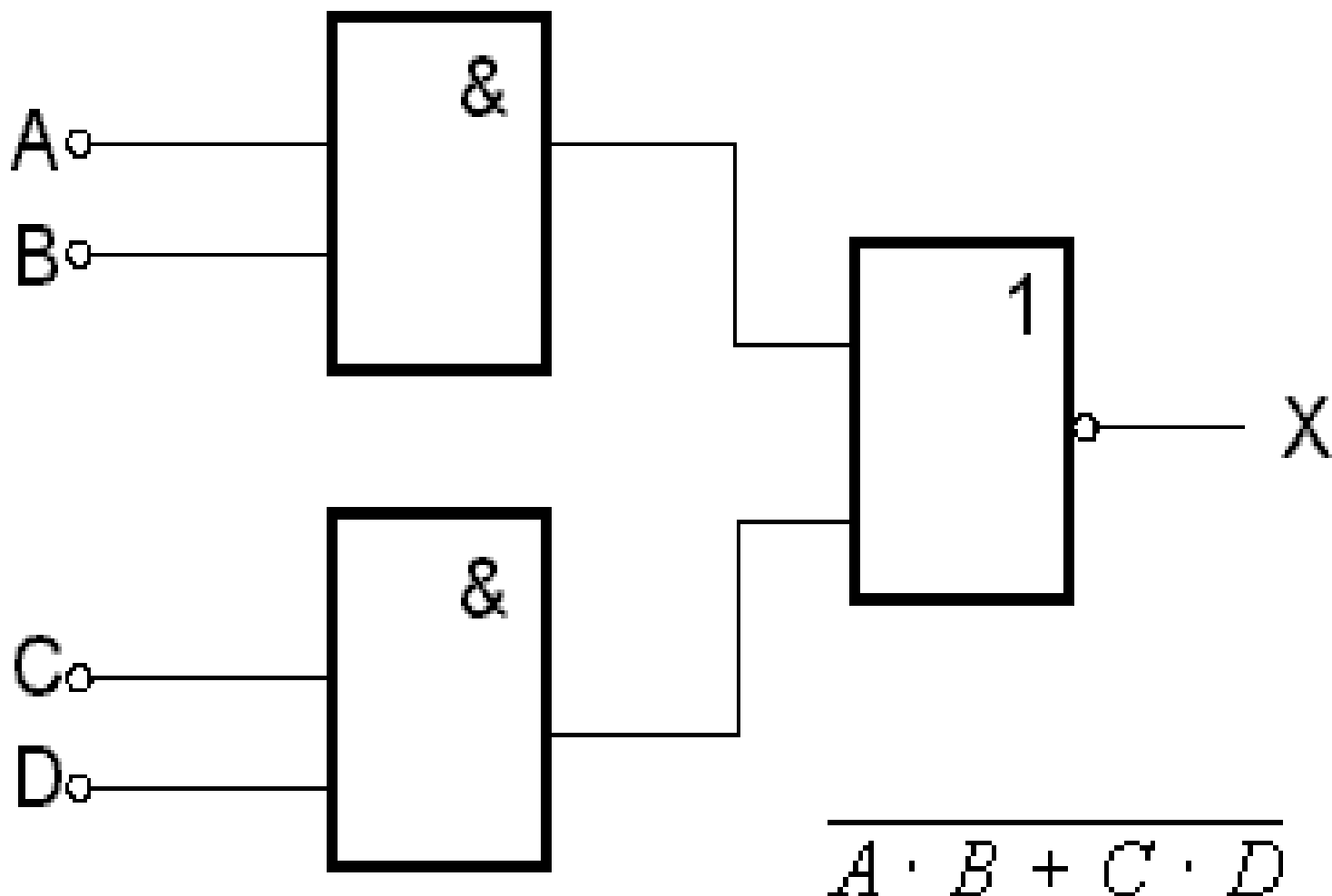
▶ $D = 1$

▶ $A = 1$

▶ $B = 1$

▶ $C = 0$

▶ $D = 0$



A	B	C	D	X
0	0	0	0	1
0	0	0	1	1
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	0
1	0	0	0	1
1	0	0	1	1
1	0	1	0	1
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

Stanovte stavy na výstupech

▶ $A = 1$

▶ $B = 0$

▶ $A = 0$

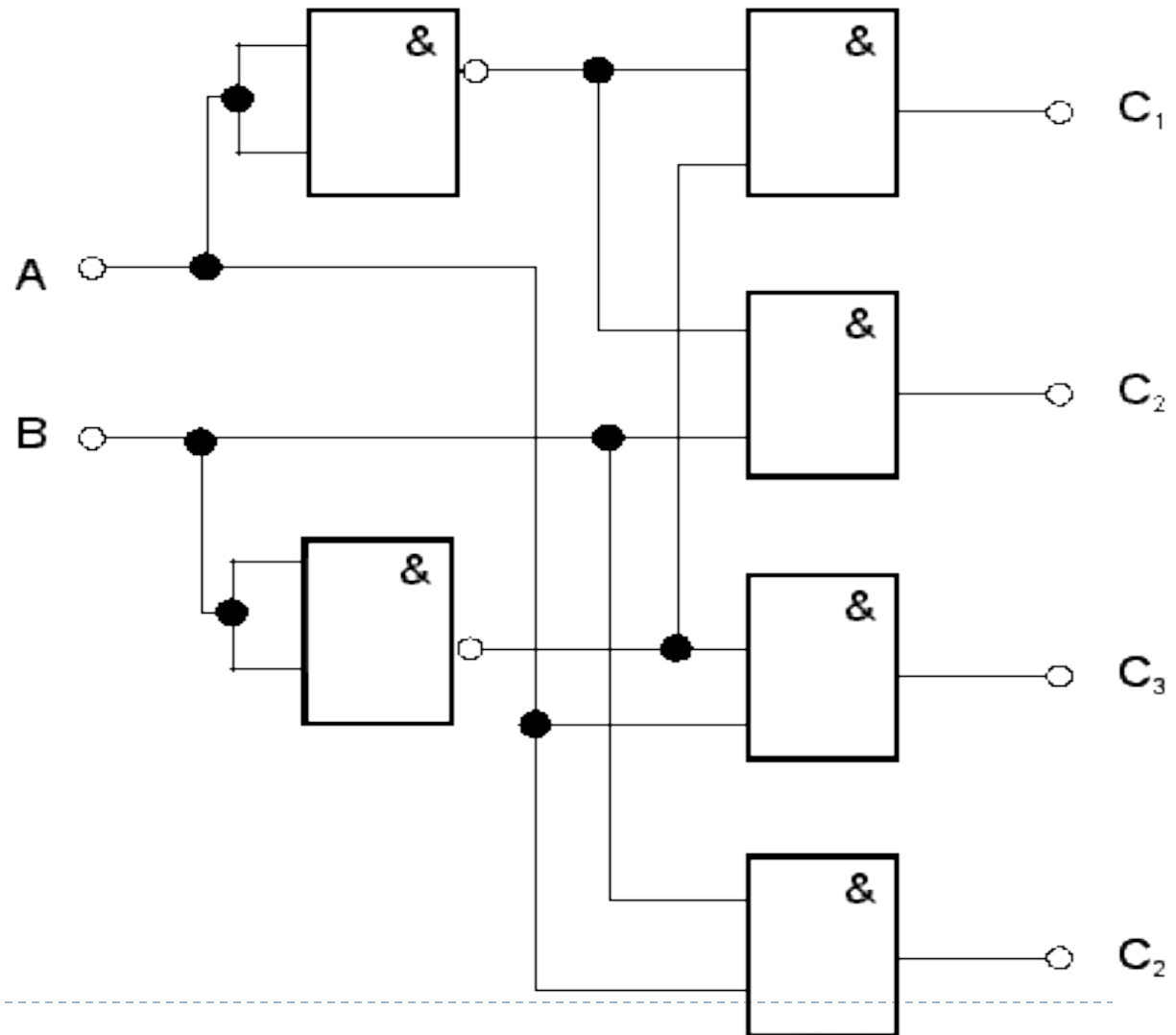
▶ $B = 0$

▶ $A = 0$

▶ $B = 1$

▶ $A = 1$

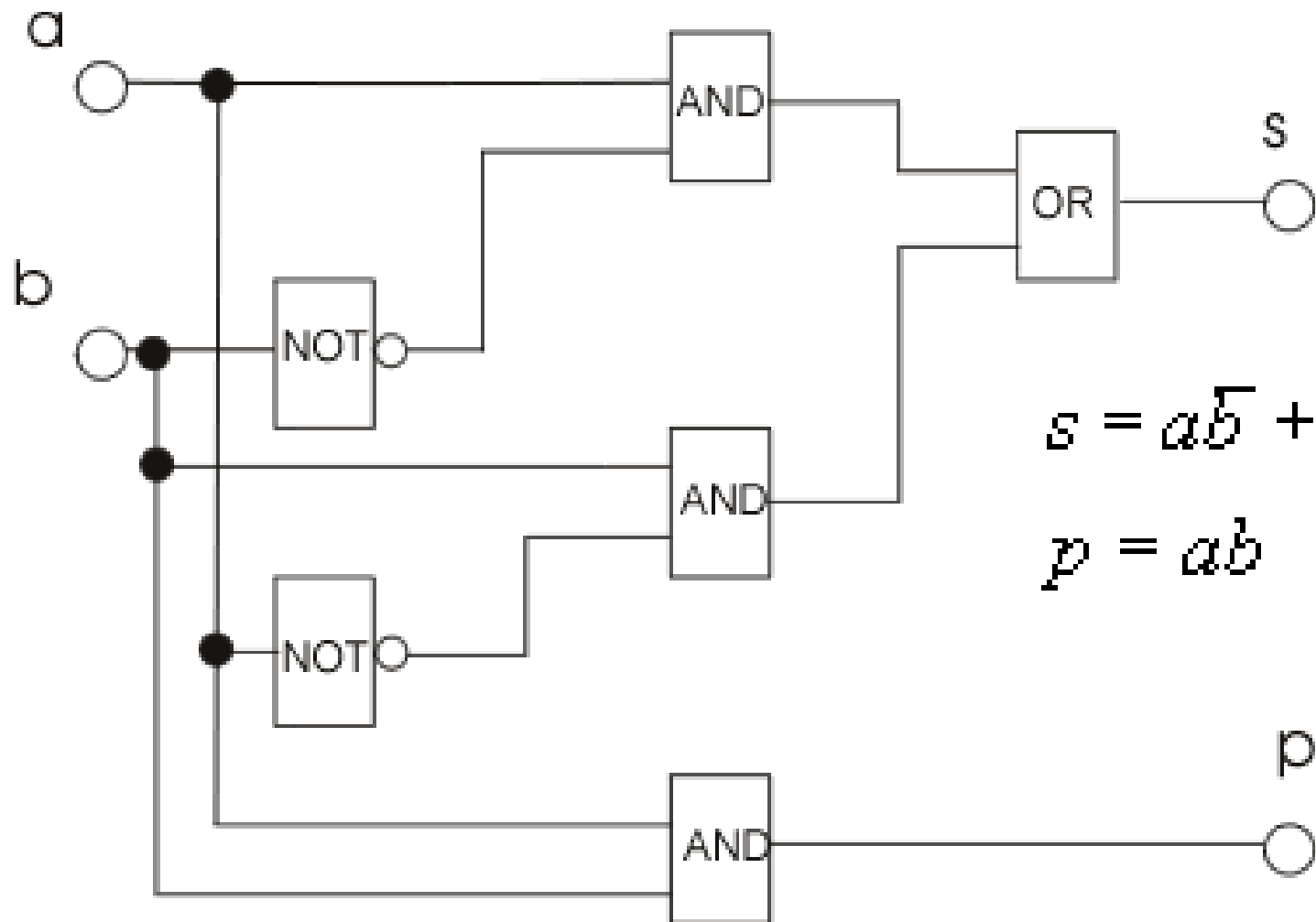
▶ $B = 1$



vstupy		výstupy			
A	B	C_1	C_2	C_3	C_4
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1



Stanovte stavy na výstupech p/s



a	b		p	s
0	0		0	0
0	1		0	1
1	0		0	1
1	1		1	0